



High power cycling capability
Low on-state and switching losses
Designed for traction and industrial applications

Phase Control Thyristor Type T243-400-44

Mean on-state current	I_{TAV}		400 A	
Repetitive peak off-state voltage	V_{DRM}		3800 ÷ 4400 V	
Repetitive peak reverse voltage	V_{RRM}			
Turn-off time	t_q		500, 630, 800 μ s	
V_{DRM}, V_{RRM}, V	3800	4000	4200	4400
Voltage code	38	40	42	44
$T_j, ^\circ C$	-60 ÷ 125			

MAXIMUM ALLOWABLE RATINGS

Symbols and parameters		Units	Values	Test conditions
ON-STATE				
I_{TAV}	Mean on-state current	A	400 460	$T_c=92^\circ C$, Double side cooled $T_c=85^\circ C$, Double side cooled 180° half-sine wave; 50 Hz
I_{TRMS}	RMS on-state current	A	628	$T_c=92^\circ C$, Double side cooled 180° half-sine wave; 50 Hz
I_{TSM}	Surge on-state current	kA	8.0 9.0	$T_j=T_{j\ max}$ $T_j=25^\circ C$ 180° half-sine wave; $t_p=10\ ms$; single pulse; $V_D=V_R=0\ V$; Gate pulse: $I_G=2\ A$; $t_{GP}=50\ \mu s$; $di_G/dt \geq 1\ A/\mu s$
			9.0 10.4	$T_j=T_{j\ max}$ $T_j=25^\circ C$ 180° half-sine wave; $t_p=8.3\ ms$; single pulse; $V_D=V_R=0\ V$; Gate pulse: $I_G=2\ A$; $t_{GP}=50\ \mu s$; $di_G/dt \geq 1\ A/\mu s$
I^2t	Safety factor	$A^2s \cdot 10^3$	320 420	$T_j=T_{j\ max}$ $T_j=25^\circ C$ 180° half-sine wave; $t_p=10\ ms$; single pulse; $V_D=V_R=0\ V$; Gate pulse: $I_G=2\ A$; $t_{GP}=50\ \mu s$; $di_G/dt \geq 1\ A/\mu s$
			335 445	$T_j=T_{j\ max}$ $T_j=25^\circ C$ 180° half-sine wave; $t_p=8.3\ ms$; single pulse; $V_D=V_R=0\ V$; Gate pulse: $I_G=2\ A$; $t_{GP}=50\ \mu s$; $di_G/dt \geq 1\ A/\mu s$
BLOCKING				
V_{DRM}, V_{RRM}	Repetitive peak off-state and Repetitive peak reverse voltages	V	3800 ÷ 4400	$T_{j\ min} < T_j < T_{j\ max}$; 180° half-sine wave; 50 Hz; Gate open
V_{DSM}, V_{RSM}	Non-repetitive peak off-state and Non-repetitive peak reverse voltages	V	3900 ÷ 4500	$T_{j\ min} < T_j < T_{j\ max}$; 180° half-sine wave; single pulse; Gate open
V_D, V_R	Direct off-state and Direct reverse voltages	V	$0.75 \cdot V_{DRM}$ $0.75 \cdot V_{RRM}$	$T_j=T_{j\ max}$; Gate open

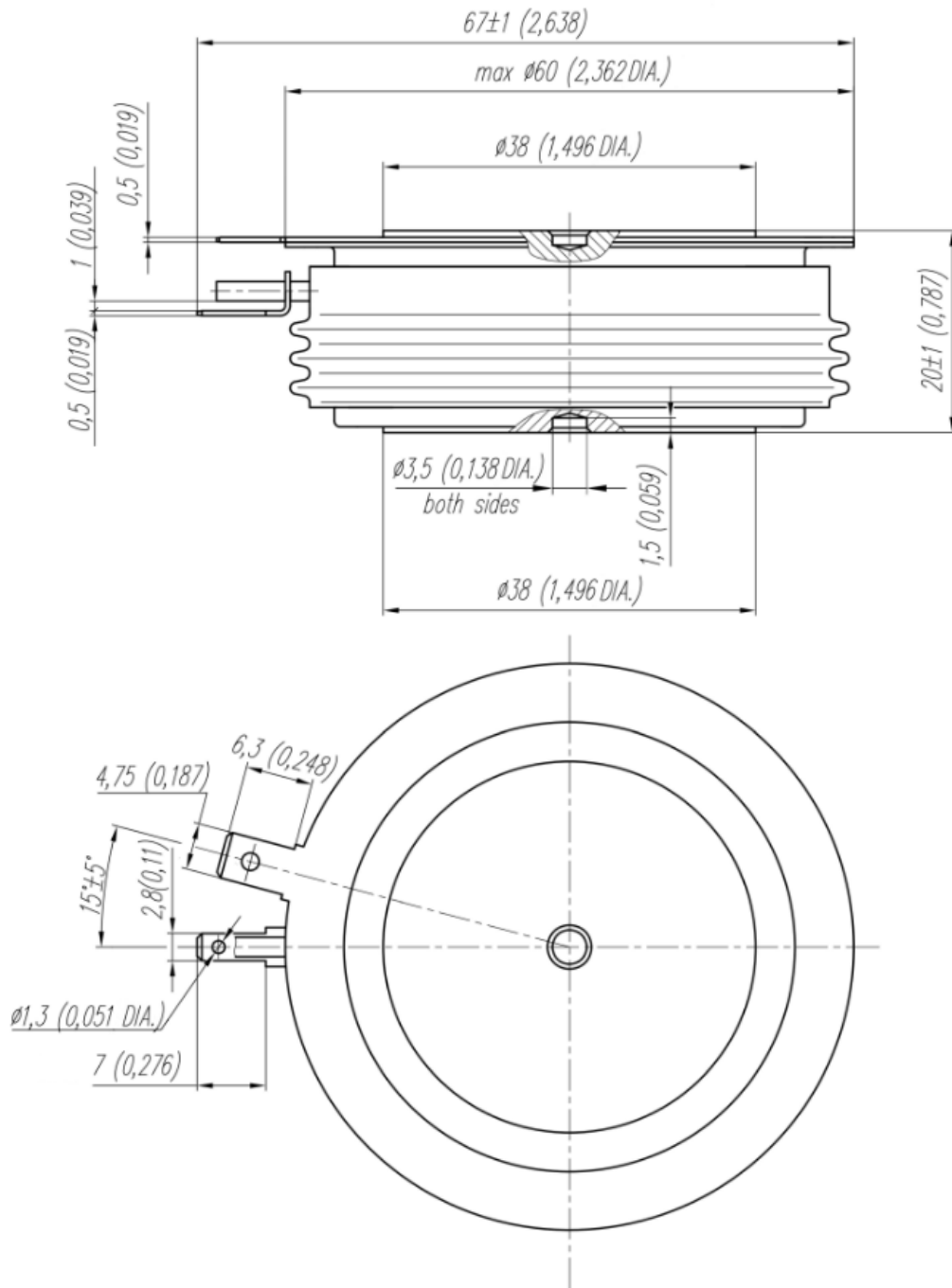
TRIGGERING				
I_{FGM}	Peak forward gate current	A	8	$T_j = T_{j\ max}$
V_{RGM}	Peak reverse gate voltage	V	5	
P_G	Gate power dissipation	W	4	$T_j = T_{j\ max}$ for DC gate current
SWITCHING				
$(di_T/dt)_{crit}$	Critical rate of rise of on-state current non-repetitive (f=1 Hz)	A/ μ s	400	$T_j = T_{j\ max}$; $V_D = 0.67 \cdot V_{DRM}$; $I_{TM} = 2 I_{TAV}$; Gate pulse: $I_G = 2$ A; $t_{GP} = 50 \mu$ s; $di_G/dt \geq 2$ A/ μ s
THERMAL				
T_{stg}	Storage temperature	$^{\circ}$ C	-60÷50	
T_j	Operating junction temperature	$^{\circ}$ C	-60÷125	
MECHANICAL				
F	Mounting force	kN	14.0÷16.0	
a	Acceleration	m/s ²	50 100	Device unclamped Device clamped

CHARACTERISTICS

Symbols and parameters		Units	Values	Conditions	
ON-STATE					
V_{TM}	Peak on-state voltage, max	V	2.35	$T_j = 25 \text{ }^{\circ}\text{C}$; $I_{TM} = 1256$ A	
$V_{T(TO)}$	On-state threshold voltage, max	V	1.30	$T_j = T_{j\ max}$;	
r_T	On-state slope resistance, max	m Ω	1.250	$0.5 \pi I_{TAV} < I_T < 1.5 \pi I_{TAV}$	
I_L	Latching current, max	mA	1000	$T_j = 25 \text{ }^{\circ}\text{C}$; $V_D = 12$ V; Gate pulse: $I_G = 2$ A; $t_{GP} = 50 \mu$ s; $di_G/dt \geq 1$ A/ μ s	
I_H	Holding current, max	mA	300	$T_j = 25 \text{ }^{\circ}\text{C}$; $V_D = 12$ V; Gate open	
BLOCKING					
I_{DRM}, I_{RRM}	Repetitive peak off-state and Repetitive peak reverse currents, max	mA	100	$T_j = T_{j\ max}$; $V_D = V_{DRM}$; $V_R = V_{RRM}$	
$(dv_D/dt)_{crit}$	Critical rate of rise of off-state voltage ¹⁾ , min	V/ μ s	200, 320, 500, 1000	$T_j = T_{j\ max}$; $V_D = 0.67 \cdot V_{DRM}$; Gate open	
TRIGGERING					
V_{GT}	Gate trigger direct voltage, max	V	5.00	$T_j = T_{j\ min}$ $T_j = 25 \text{ }^{\circ}\text{C}$ $T_j = T_{j\ max}$	$V_D = 12$ V; $I_D = 3$ A; Direct gate current
			3.00		
			2.00		
I_{GT}	Gate trigger direct current, max	mA	500	$T_j = T_{j\ min}$ $T_j = 25 \text{ }^{\circ}\text{C}$ $T_j = T_{j\ max}$	
			300		
			200		
V_{GD}	Gate non-trigger direct voltage, min	V	0.25	$T_j = T_{j\ max}$;	Direct gate current
I_{GD}	Gate non-trigger direct current, min	mA	10.00	$V_D = 0.67 \cdot V_{DRM}$;	
SWITCHING					
t_{gd}	Delay time	μ s	3.50	$T_j = 25 \text{ }^{\circ}\text{C}$; $V_D = 0.4 \cdot V_{DRM}$; $I_{TM} = I_{TAV}$; Gate pulse: $I_G = 2$ A; $t_{GP} = 50 \mu$ s; $di_G/dt \geq 2$ A/ μ s	
t_q	Turn-off time ²⁾ , max	μ s	500, 630, 800	$dv_D/dt = 50$ V/ μ s; $T_j = T_{j\ max}$; $I_{TM} = I_{TAV}$; $di_R/dt = -10$ A/ μ s; $V_R = 100$ V; $V_D = 0.67 \cdot V_{DRM}$	
Q_{rr}	Total recovered charge, max	μ C	2000	$T_j = T_{j\ max}$; $I_{TM} = 400$ A;	
t_{rr}	Reverse recovery time, max	μ s	50	$di_R/dt = -5$ A/ μ s;	
I_{rrM}	Peak reverse recovery current, max	A	80	$V_R = 100$ V	

THERMAL					
R_{thjc}	Thermal resistance, junction to case, max	°C/W	0.0320	Direct current	Double side cooled
R_{thjc-A}			0.0704		Anode side cooled
R_{thjc-K}			0.0576		Cathode side cooled
R_{thck}	Thermal resistance, case to heatsink, max	°C/W	0.0060	Direct current	
MECHANICAL					
w	Weight, typ	g	260		
D_s	Surface creepage distance	mm (inch)	19.44 (0.765)		
D_a	Air strike distance	mm (inch)	12.10 (0.476)		

PART NUMBERING GUIDE							NOTES																					
T	243	400	44	A2	E2	N	¹⁾ Critical rate of rise of off-state voltage <table border="1"> <thead> <tr> <th>Symbol of Group</th> <th>P2</th> <th>K2</th> <th>E2</th> <th>A2</th> </tr> </thead> <tbody> <tr> <td>$(dv_D/dt)_{crit}, V/\mu s$</td> <td>200</td> <td>320</td> <td>500</td> <td>1000</td> </tr> </tbody> </table> ²⁾ Turn-off time ($dv_D/dt=50 V/\mu s$) <table border="1"> <thead> <tr> <th>Symbol of Group</th> <th>E2</th> <th>C2</th> <th>B2</th> </tr> </thead> <tbody> <tr> <td>$t_q, \mu s$</td> <td>500</td> <td>630</td> <td>800</td> </tr> </tbody> </table>				Symbol of Group	P2	K2	E2	A2	$(dv_D/dt)_{crit}, V/\mu s$	200	320	500	1000	Symbol of Group	E2	C2	B2	$t_q, \mu s$	500	630	800
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1. Phase Control Thyristor 2. Design version 3. Mean on-state current, A 4. Voltage code 5. Critical rate of rise of off-state voltage, V/ μs 6. Turn-off time ($dv_D/dt=50 V/\mu s$) 7. Ambient conditions: N – normal; T – tropical																												



All dimensions in millimeters (inches)

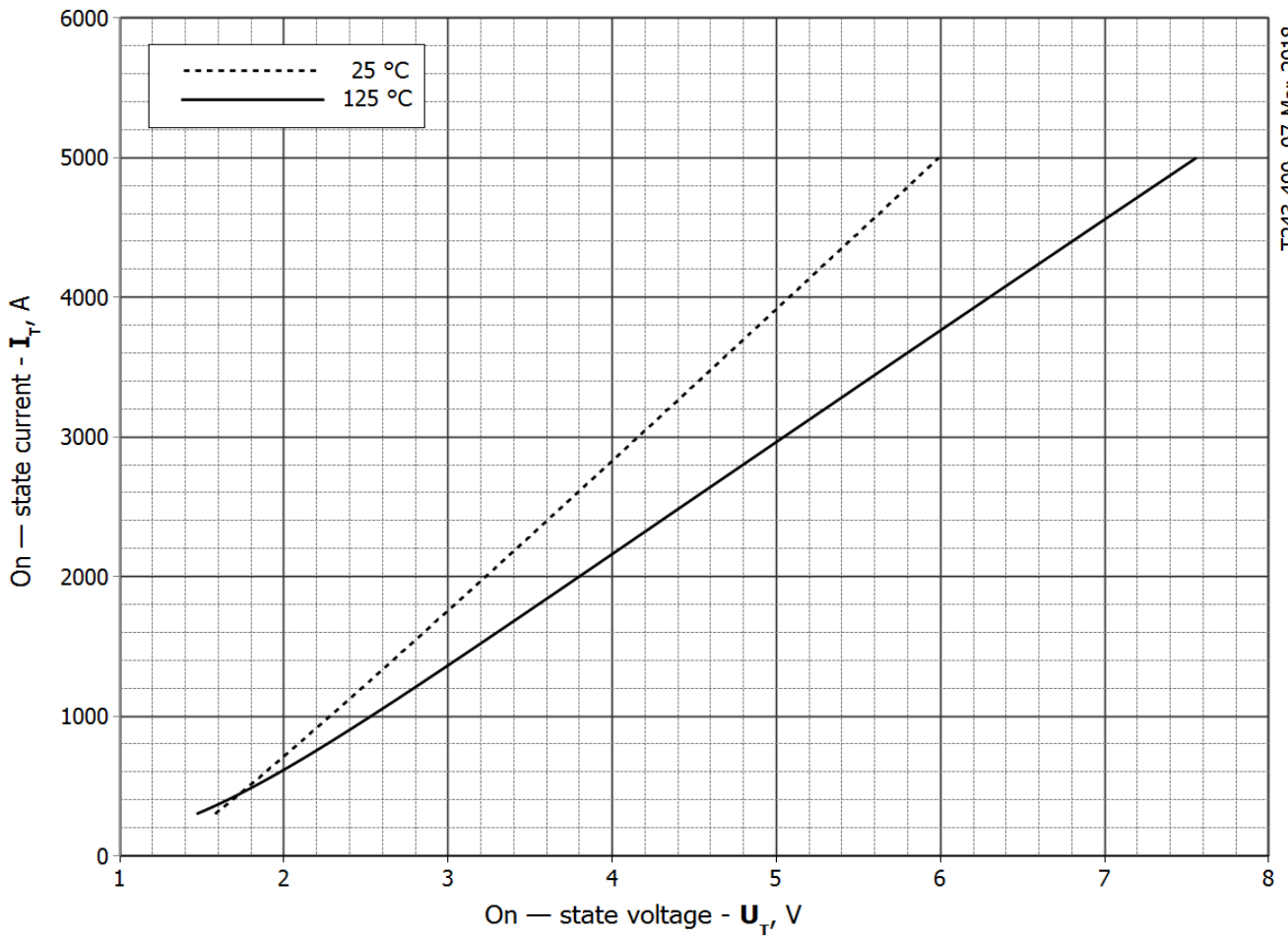


Fig 1 – On-state characteristics of Limit device

Analytical function for On-state characteristic:

$$V_T = A + B \cdot i_T + C \cdot \ln(i_T + 1) + D \cdot \sqrt{i_T}$$

	Coefficients for max curves	
	$T_j = 25^\circ\text{C}$	$T_j = T_{j,max}$
A	1.1624	-1.4703109
B	0.00087038	0.0014844
C	0.011052	0.5795044
D	0.0054011	-0.0470629

On-state characteristic model (see Fig. 1)

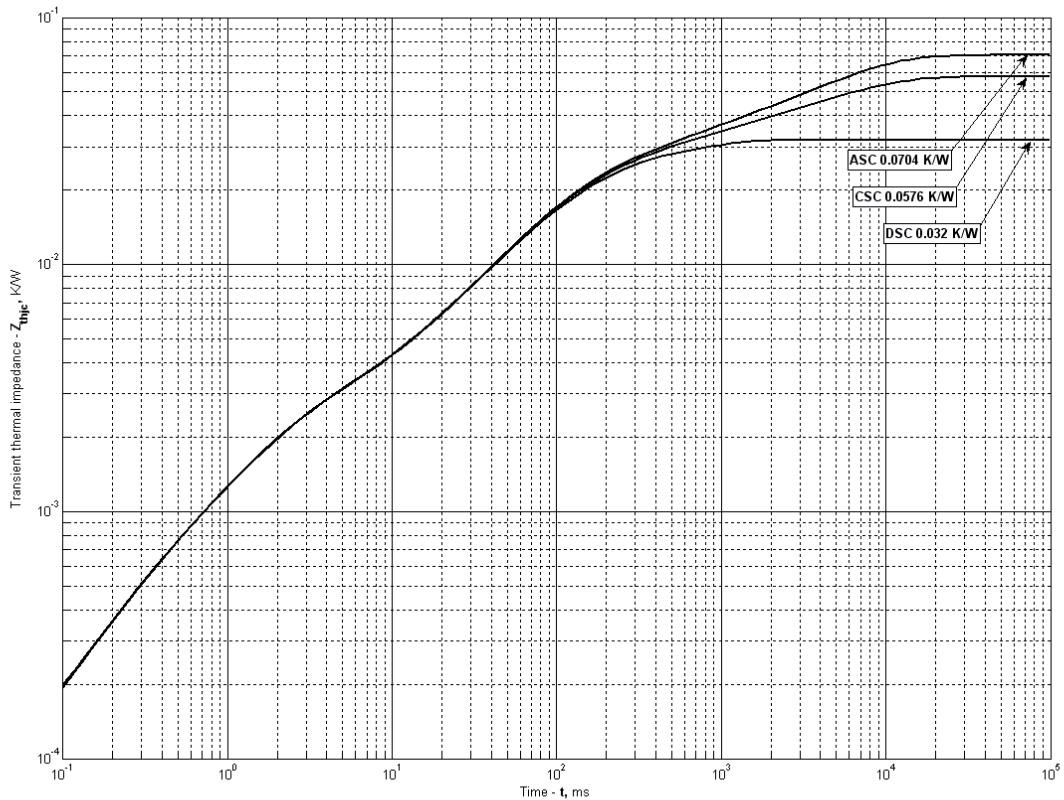


Fig 2 – Transient thermal impedance

Analytical function for Transient thermal impedance junction to case Z_{thjc} for DC:

$$Z_{thjc} = \sum_{i=1}^n R_i \left(1 - e^{-\frac{t}{\tau_i}} \right)$$

Where $i = 1$ to n , n is the number of terms in the series.

t = Duration of heating pulse in seconds.

Z_{thjc} = Thermal resistance at time t .

R_i = Amplitude of p_{th} term.

τ_i = Time constant of r_{th} term.

DC Double side cooled

i	1	2	3	4	5	6
R_i, K/W	0.000005619	0.01031	0.01922	0.0004148	0.001895	0.0001521
τ_i, s	7.790	0.5094	0.09719	0.01725	0.0016	0.0002257

DC Anode side cooled

i	1	2	3	4	5	6
R_i, K/W	0.0381	0.008681	0.01867	0.001961	0.0001787	0.002771
τ_i, s	5.351	0.4584	0.09325	0.001734	0.0002174	0.9059

DC Cathode side cooled

i	1	2	3	4	5	6
R_i, K/W	0.02561	0.001472	0.01786	0.001926	0.0001928	0.01052
τ_i, s	5.328	0.1832	0.09031	0.001714	0.0002598	0.525

Transient thermal impedance junction to case Z_{thjc} model (see Fig. 2)

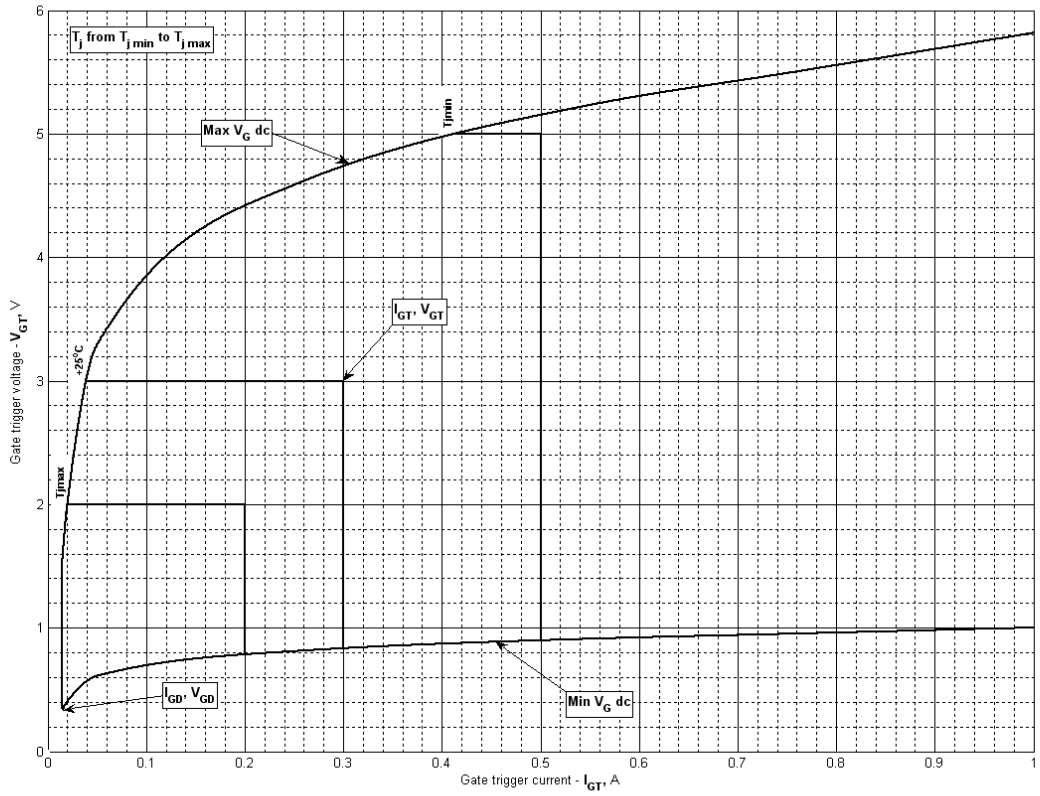


Fig 3 – Gate characteristics – Trigger limits

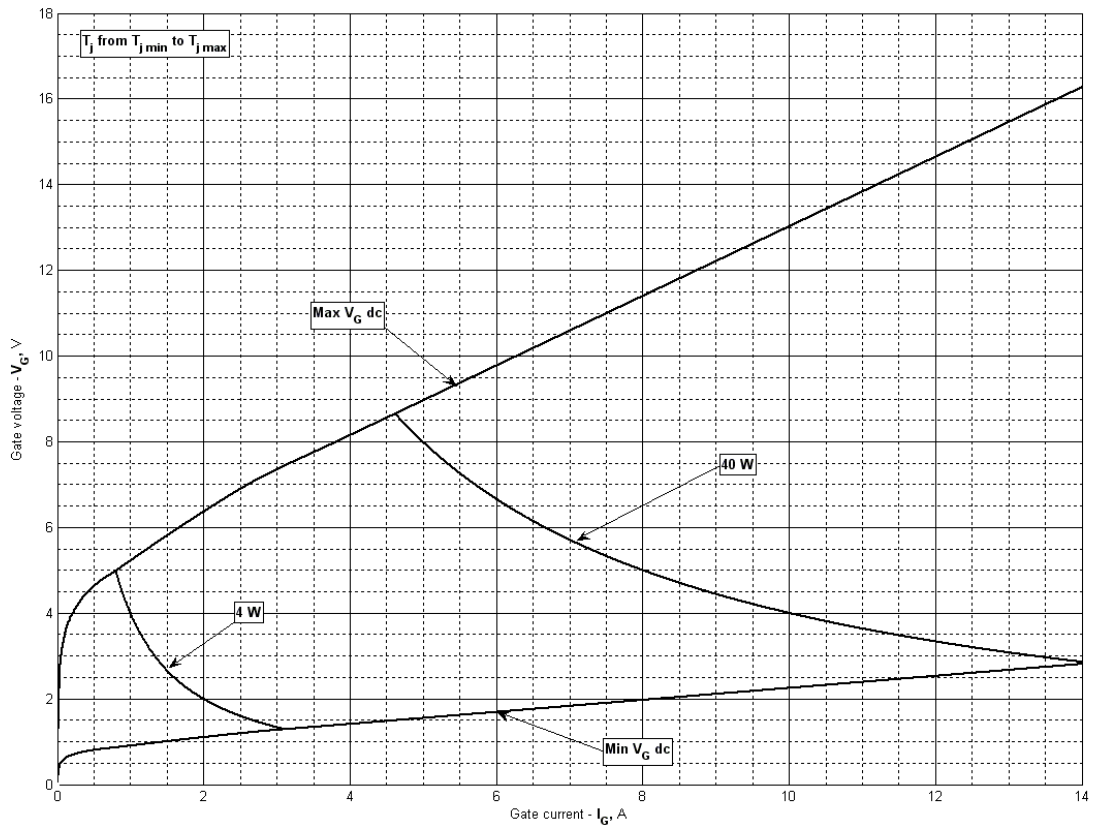


Fig 4 - Gate characteristics –Power curves

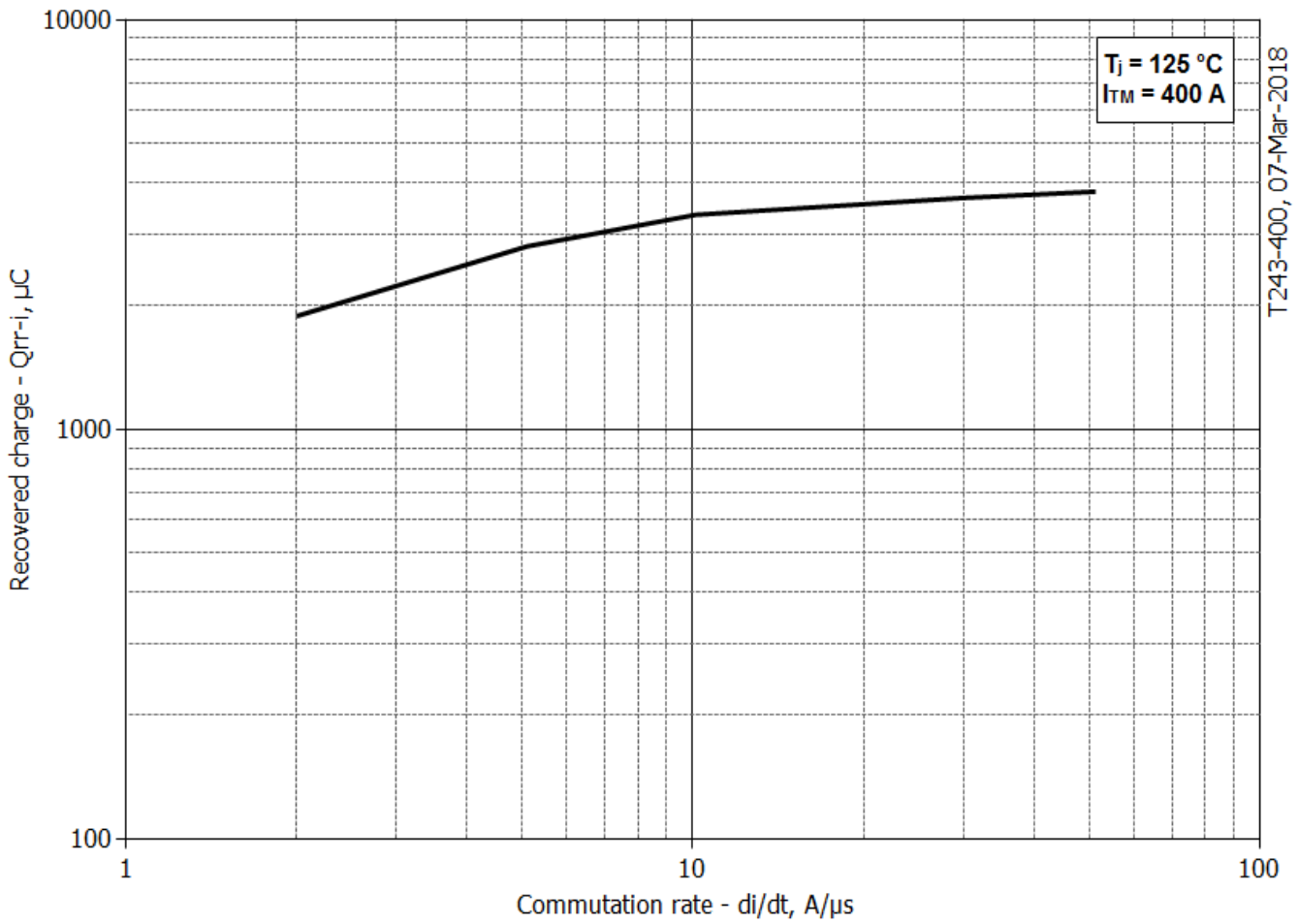


Fig 5 – Total recovered charge, Q_{rr-i} (integral)

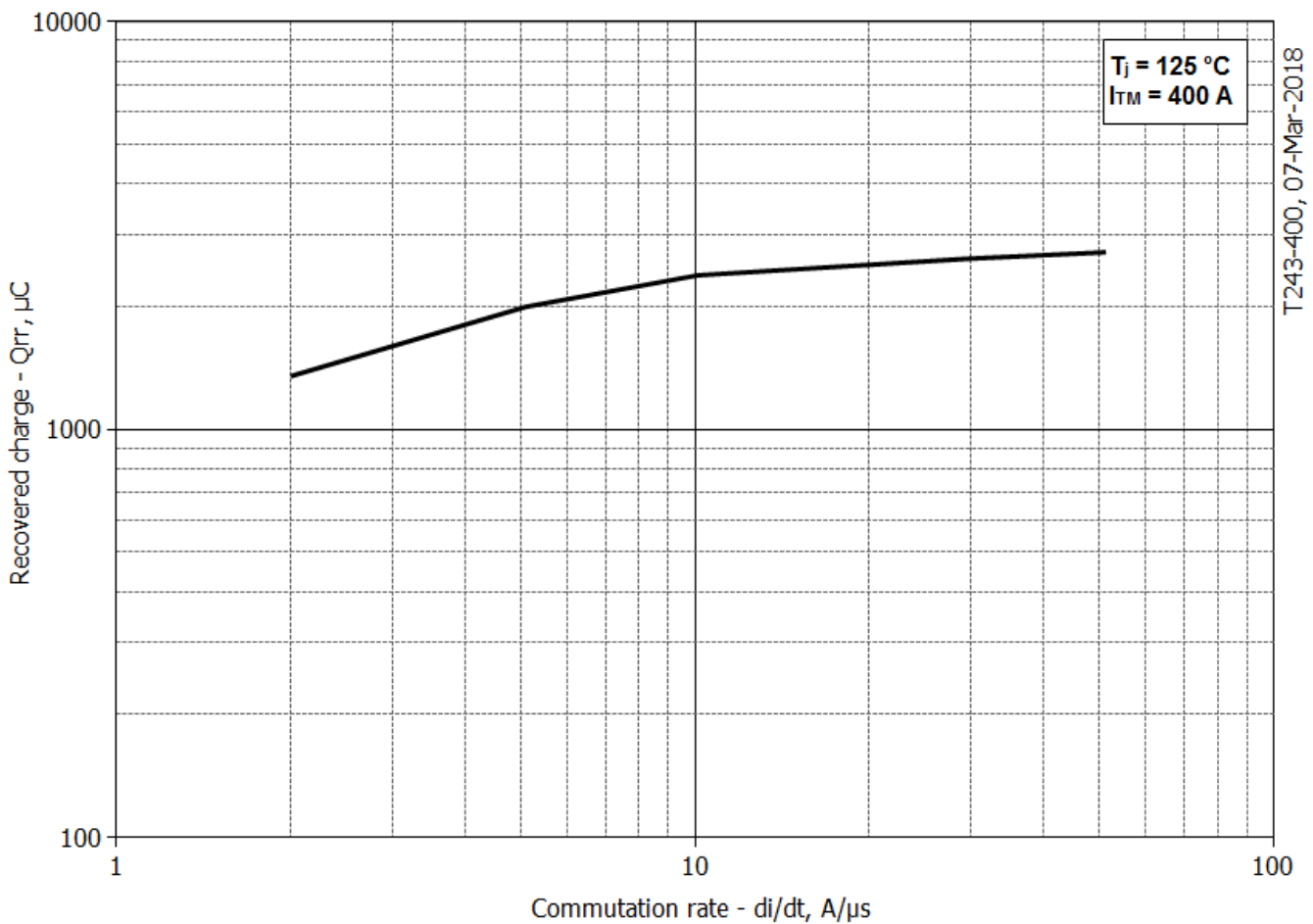
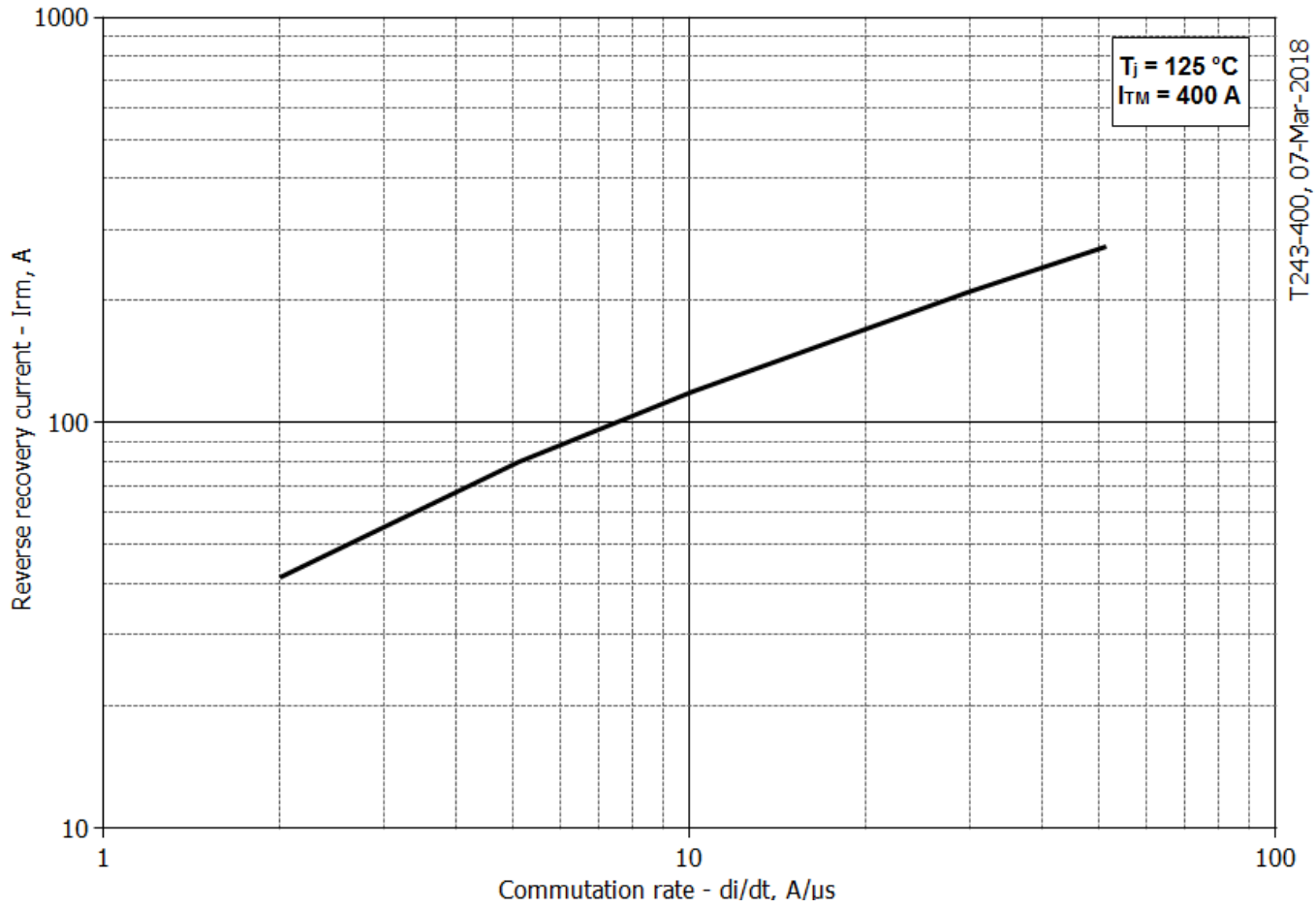
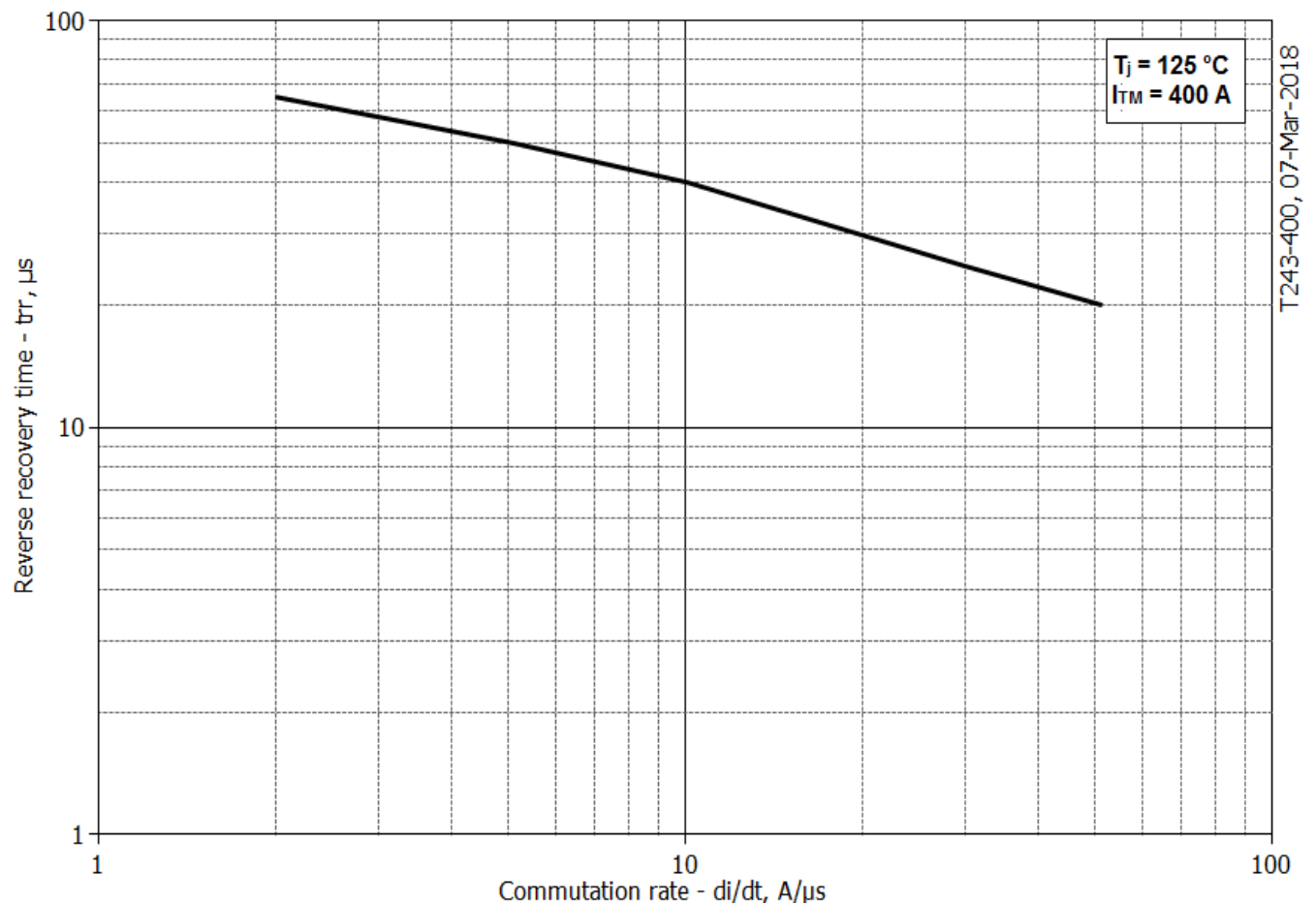


Fig 6 - Recovered charge, Q_{rr} (linear)



T243-400, 07-Mar-2018

Fig 7 – Peak reverse recovery current, I_{rm}



T243-400, 07-Mar-2018

Fig 8 – Maximum recovery time, t_{rr} (linear)

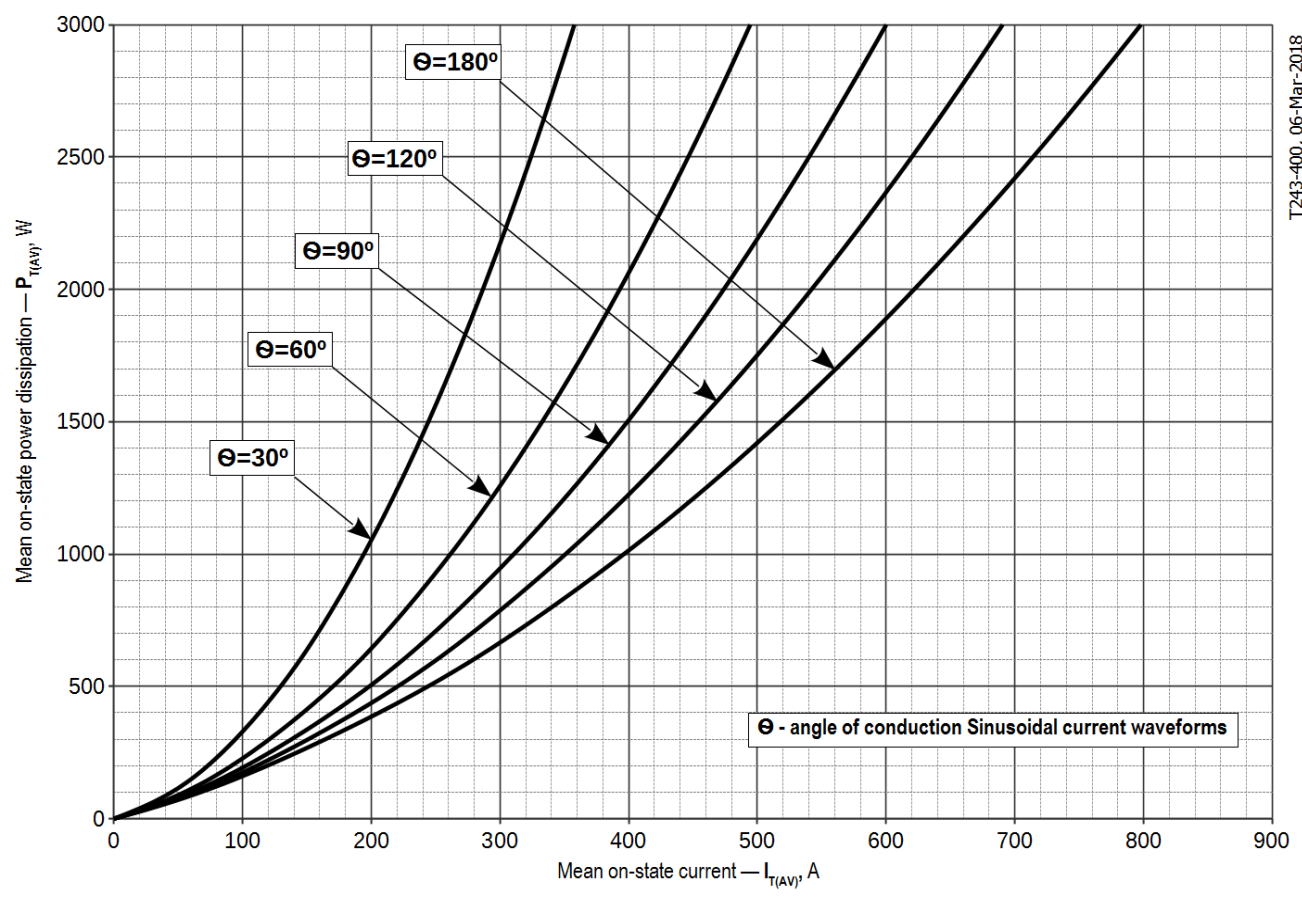


Fig 9 – On-state power loss (sinusoidal current waveforms)

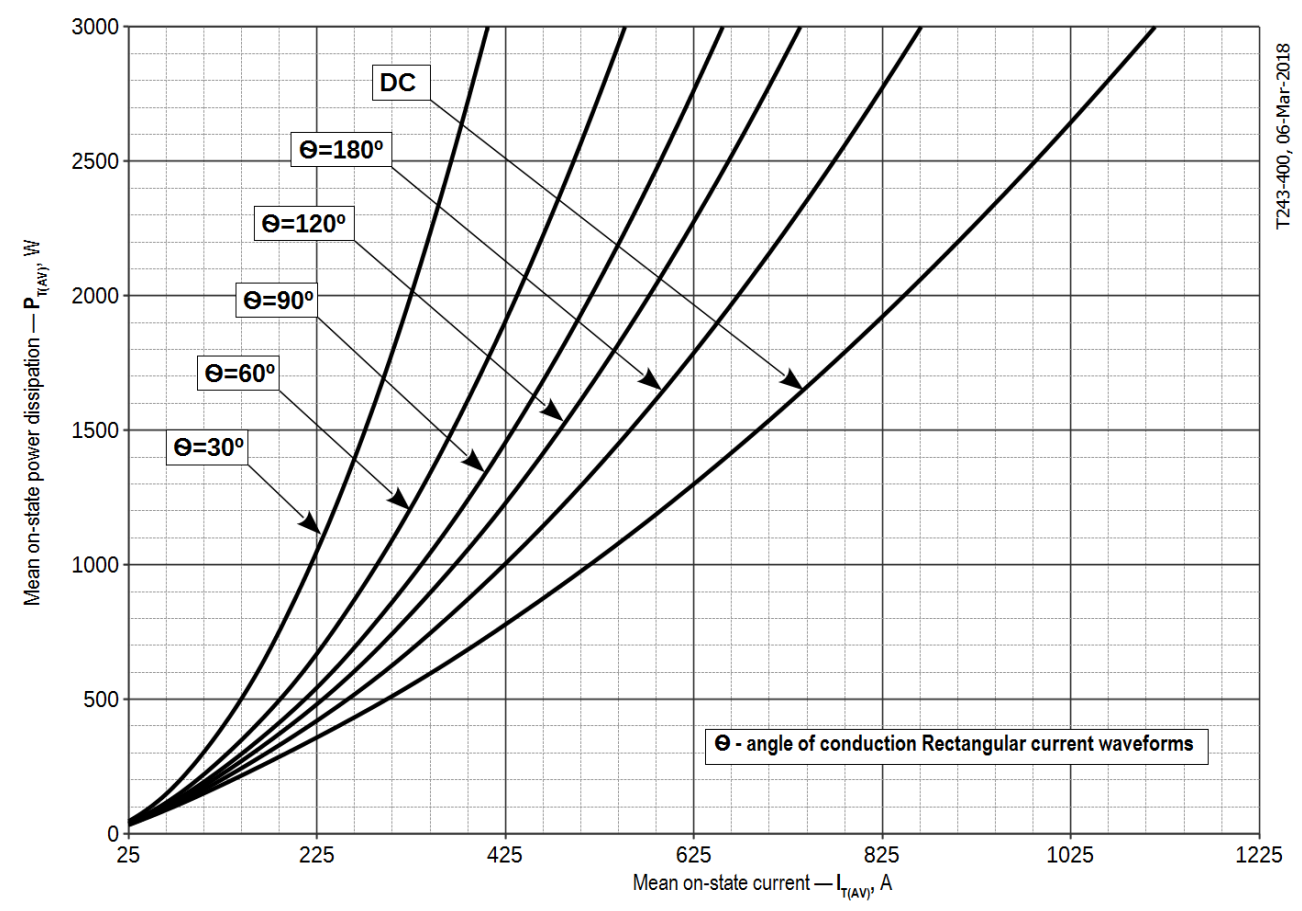


Fig 10 – On-state power loss (rectangular current waveforms)

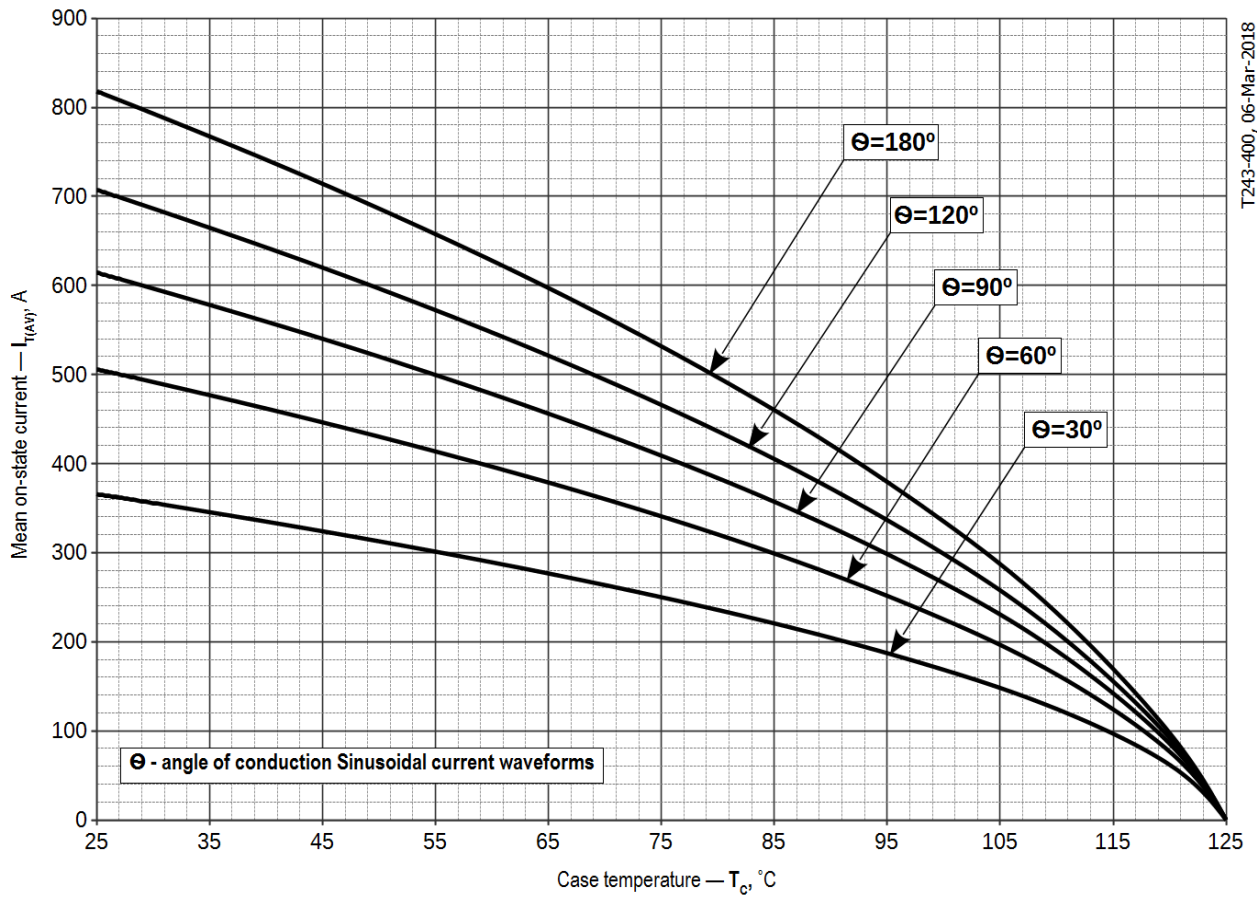


Fig 11 – Maximum case temperature DSC (sinusoidal current waveforms)

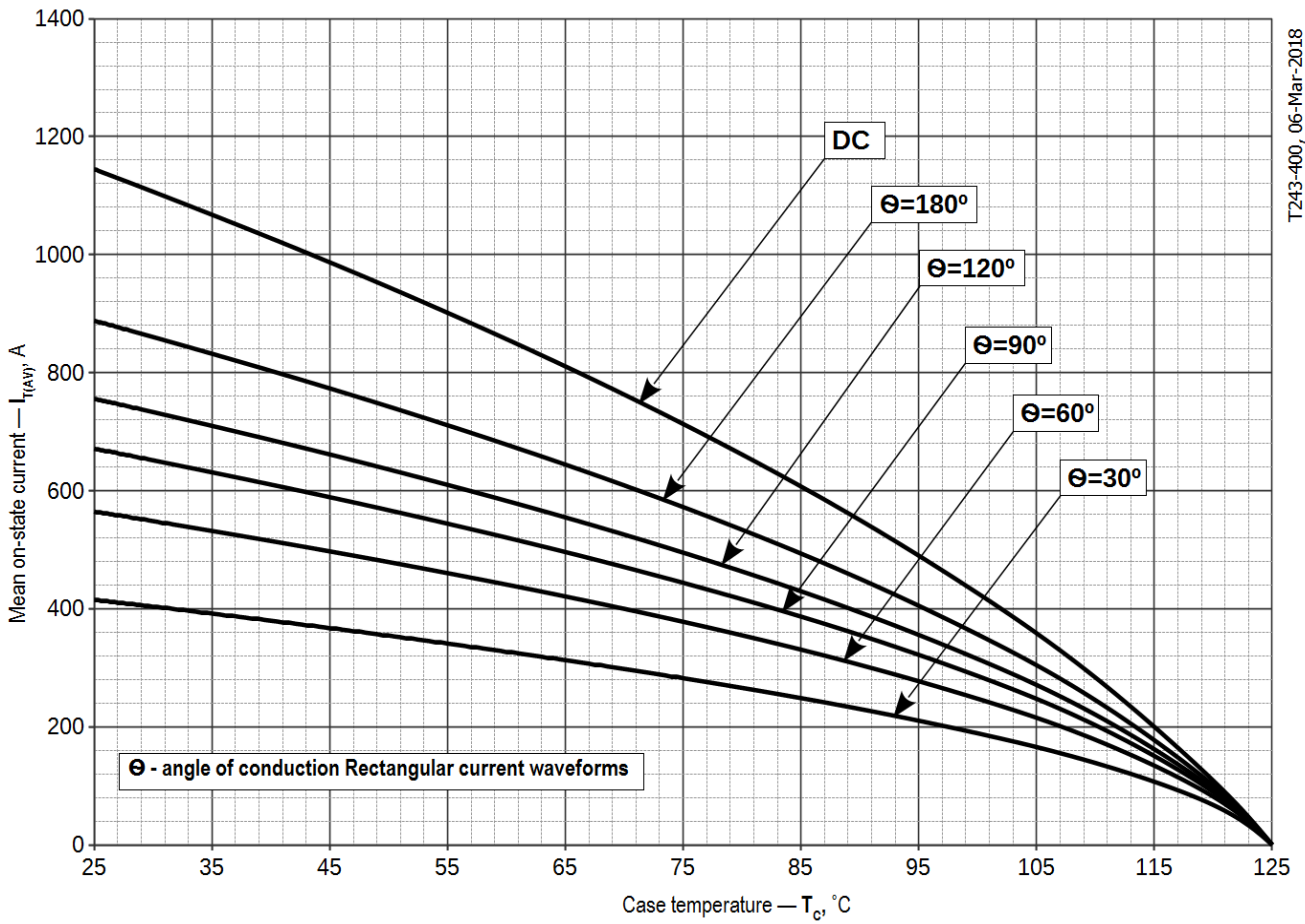


Fig 12 – Maximum case temperature DSC (rectangular current waveforms)

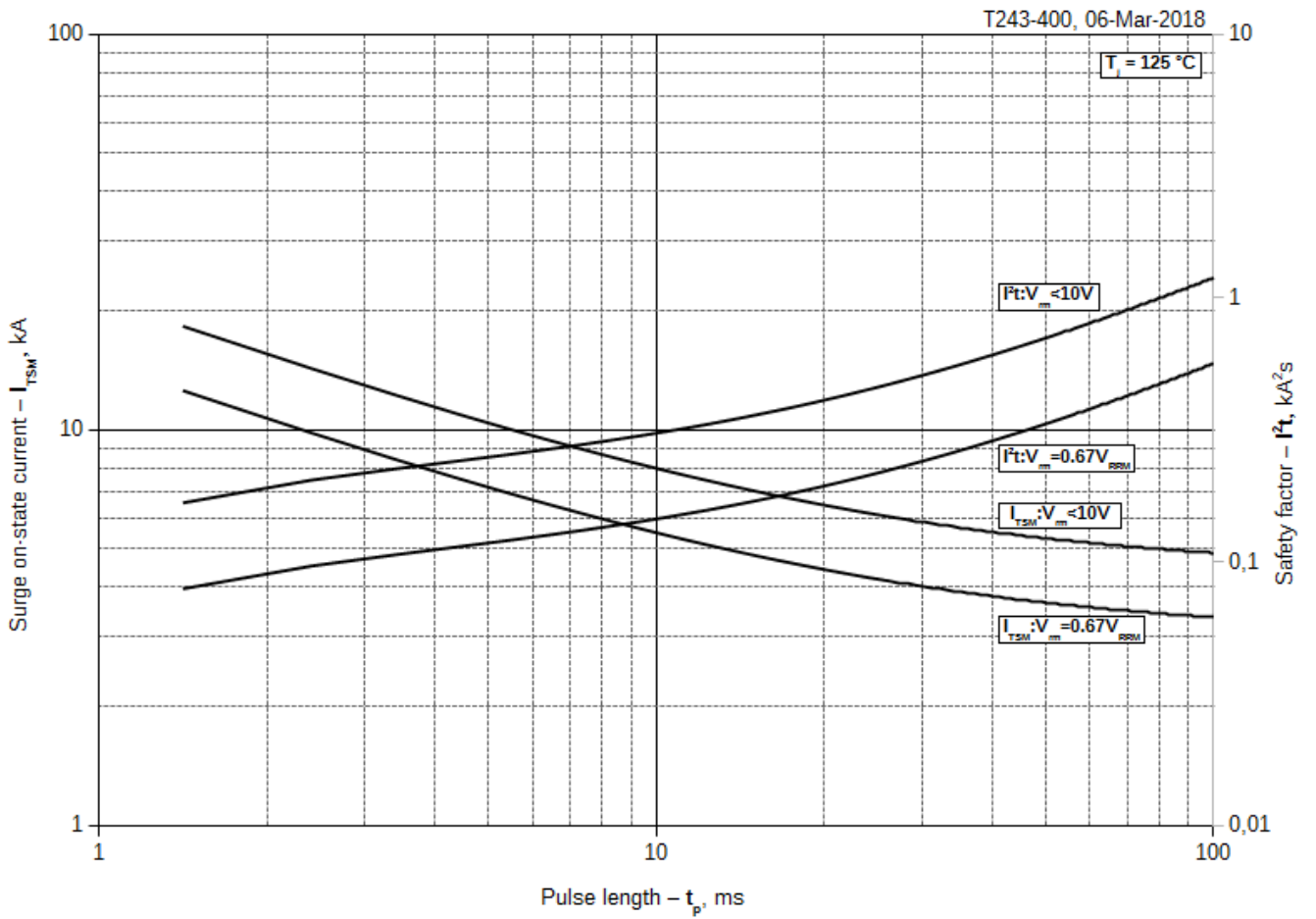


Fig 13 – Maximum surge and I^2t ratings

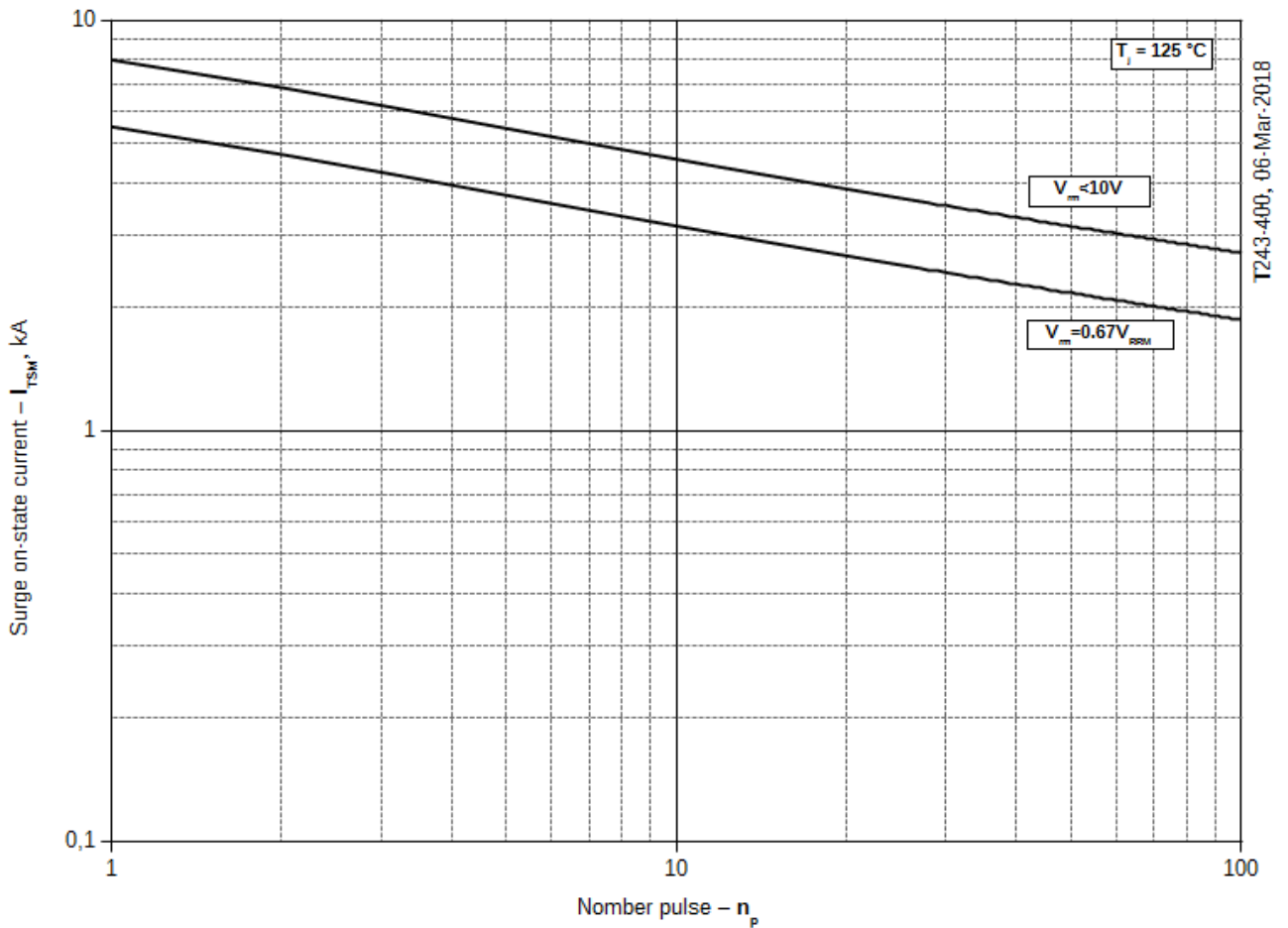


Fig 14 – Maximum surge ratings